The 8-bit computer design was tested with Quartus II version 7.2 Sp2, on the DE2 card. The project can be restored with the Project > Restore Archived Project command. The startup module was replaced with a synchronous reset. The reset switch is Key0, and the seven segment displays are Hex 6 & 7. The clock is provided by connecting an external 10 Hertz, 3.3Volt function generator to the SMA connector.