GlobalFoundries is currently looking to fill the following position

We are looking for [a JL6 (Principal Corp AE)](https://gfoundries.taleo.net/careersection/gf_ext/jobdetail.ftl?job=22000032&tz=GMT-06%3A00&tzname=America%2FChicago) with the minimum following skill set:

* BSEE with at least 2 years of relevant experience or MSEE
* BS/MS/PHD students will be considered.
* Experience in at least one of the following:
  + Analog or RF circuit design experience or MSEE projects on Analog/RF circuit design
  + Analog simulation tool support using Cadence Spectre or Synopsys Hspice
  + Cadence Virtuoso tools (VXL, Pcells, CDF, Spectre)
* Proce​ss technology background
* Familiarity with Analog Mixed-Signal EDA design flow from design concept to sign-off
* Experience in UNIX / Linux OS
* Strong communication skills both oral and written (English)
* Must enjoy working with customers

We would prefer the candidate be located in the bay area (Santa Clara) but open to any other place in CA or even Austin, TX.