

Amazing people make
amazing things happen.

Join our community of innovators!

Apply at [Intel.com/jobs](https://www.intel.com/jobs) today!

Join Us as we Fuel Intel's Growth!

We partner with the Semiconductor, IP and EDA eco-systems to leverage an incomparable mix of experts with different backgrounds, cultures, perspectives, and experiences delivering a world-class design system that powers Intel's leadership products.

What We Do

The Product Enablement Solutions Group (PESG) is a worldwide organization that delivers an end-to-end design system for development of IPs, Cores and SOCs.

We Want You!

Our leaders want to fuel your growth and development. We are committed to fostering a fun, inclusive and diverse culture that creates your best work experience.

Key Opportunities

- EDA Tools Software Eng
- CAD Intern
- Physical Design Eng Intern
- DFT Engineering Intern
- Design Automation Engineering Intern
- Formal Verification Eng
- Graduate Intern Technical
- Software Eng Intern
- Memory DFT Intern



Just imagine
what we can do.



At work and in the community, PESG creates opportunities to connect and collaborate.



intel.

Do something wonderful.™

Apply at www.intel.com/jobs today!

Requisition	Job Title	Location
JR0182911	EDA Tools Software Engineer	California, Folsom
JR0183307	Formal Verification Engineer	Oregon, Hillsboro
JR0183767	Computer Aided Design Intern	Oregon, Hillsboro
JR0186549	Formal Verification Intern	Oregon, Hillsboro
JR0186022	Design Automation Engineer Intern	California, Folsom
JR0186023*	Design Automation Engineer Intern	Oregon, Hillsboro
JR0186024*	Design Automation Engineer Intern	California, Santa Clara
JR0186025*	Design Automation Engineer Intern	Texas, Austin
JR0186026*	Design Automation Engineer Intern	Massachusetts, Hudson
JR0186027*	Design Automation Engineer Intern	Colorado, Fort Collins
JR0186551	Physical Design Engineer Intern	California, Santa Clara
JR0186553	Software Engineer Intern Undergrad	Oregon, Hillsboro
JR0186554	Graduate Intern Technical	California, Santa Clara
JR0186555	Memory DFT Intern	California, Santa Clara
JR0186556*	DFT Engineering Intern – Graduate	California, Santa Clara
JR0185441*	Design Automation Engineer	California, Folsom
JR0176375*	Design Automation Engineer	Oregon, Hillsboro
JR0183308*	Design Automation Engineer	Colorado, Fort Collins
JR0182911*	EDA Tools Software Engineer	California, Folsom