Questions:

What is the effective voltage when two voltage sources are in series?
What happens when two voltage sources are placed in parallel?
What is the effective current when two current sources are in parallel?
What happens when two current sources are placed in series?
What is the effect of placing a resistor in parallel with a voltage source?
What is the effect of placing a resistor in series with a current source?
What is node analysis?
What nodes are constrained in node analysis?
How many independent equations are needed in node analysis?

What is mesh analysis?

What loop currents are constrained in mesh analysis?

How many independent equations are needed in mesh analysis? (a little bit more tricky to answer)

Circuit Reduction - review problem



a) If we solved this circuit using the method in homework 1, how many independent equations are needed to determine the voltage across each resistor?

b) How many nodes are in the above circuit? (Answer: five)

c) Use circuit reduction techniques to find the voltage across each resistor. (*Answer: VR1 = 3V, VR2=VR3 = 2V, VR4 = 4V, VR5 = 3V, VR6 = VR7 = VR8 = 1V*)



a) Use source transformation techniques to obtain a simplified circuit with Vs,Rs and RLoad (Answer: Vs = 4V, Rs = $1k\Omega$, RLoad = $3k\Omega$)

Simple mesh/node analysis



- a) How many nodes in the circuit? (Answer: two) How many mesh loops? (Answer: two)
- b) Use node analysis to find VR1 and IR1. (Answer: 10V and 2A)
- c) Use mesh analysis to find VR1 and IR (Answer: 10V and 2A, it better be the same)



- a) How many nodes in the circuit? (Answer: two) How many mesh loops? (Answer: two)
- b) Use node analysis to find VR1 and IR1. (Answer: 5V and 1A)
- c) Use mesh analysis to find VR1 and IR1(Answer: 5V and 1A, it better be the same))

Mesh/Node



- a) How many nodes are in the above circuit? loops? (Answer: 4 nodes, 4 loops)
- b) How many nodes are constrained? (Answer: none)
- c) How many loops are constrained? (Answer: one)
- d) Is node analysis or mesh analysis easier in this circuit? (Answer: node analysis yields three independent equations, mesh analysis yields three independent equations(without circuit reduction))
- e) Determine VR3 using either mesh or node analysis. (Answer: 6V, Node analysis, V1 = 75V, V2 = 15V, V3 = 9V, V4 = 0V)
 Mesh analysis: i1 = 6mA, i2 = 3mA, i3 = 1.5mA, i4 = 0.375mA)



- a) How many nodes are in the above circuit? loops? (Answer: 5 nodes, 4 loops)
- b) How many nodes are constrained? (*Answer: two*)
- c) How many loops are constrained? (*Answer: one*)
- d) Is KCL or KVL easier in this circuit? (*Answer: same number of independent equations with either approach*)
- e) Apply the method you (we) chose to find VR9. (*Answer: VR9 = 0.47V*, Node analysis, V1 =4V, V2 = 3.58V, V3 = 1.57V, V4 = 2.04V, V5 = 0V) Mesh analysis: i1 = 1mA, i2 = 1.11mA, i3 = 2.09mA, i4 = 1.57mA)