LABORATORY 3: Phasors, Complex Power, and Filters

Note: If your partner is no longer in the class, please talk to the instructor.

Material covered:

- Phasors
- Complex power, real power, reactive power
- First Order Filters
- Multi-stage Circuits
- Bode Plots
- Filter Design Problem

Part A: Phasors

Overview Notes:

Impedance revisited

Resistors:



Capacitors



Component Time Domain		Phasor Form	
Resistor	$V_{R}(t)=I_{R}(t)R$	$VR(t)=I_R(t)R$	
Inductor	$V_L(t)=L dI_L(t)/dt$	$V_{L}(t) = (j\omega L)I_{L}(t)$	
Capacitor	$I_{\rm C}(t) = C \ dV_{\rm C}(t)/dt$	$V_{\rm C}(t) = I_{\rm C}(t)/(j\omega C)$	

Recall, circuit analysis using impedance is identical to that with resistors. The only difference is the numbers are now complex.

<u>Complex notation</u>: Recall, we can express a sinusoidal signal in the time domain as as $V(t) = A\cos(\omega t + \phi)$ where A is the amplitude of the signal, ω is the radial frequency and φ is the phase shift. The equivalent signal in phasor notation is written as $V = A \angle \phi$ where the ωt term is implied. Alternatively, polar notation can be used $V = Ae^{j\phi}$ where the real part of the polar expansion is the measureable

signal (voltage, current). We can also use the rectangular form for complex numbers, $V = A_R + jA_I$, where A_R and A_I are the real and imaginary components, respectively. The relationships between phasor and rectangular form can be written as

Phasor	Rectangular
Magnitude: $A = (A_R^2 + A_I^2)^{\frac{1}{2}}$	Real: $A_R = A\cos(\phi)$
Phase: $\phi = \tan^{-1} \left(\frac{A_I}{A_R} \right)$	Imaginary: $A_I = A\sin(\phi)$

Measurements:



The above circuit is implemented using the Vsin component as the source component. Using a source voltage of 5 V at 5 kHz, a plot of the source voltage and the capacitor voltage are shown below.



One of the issues with LTSpice is that it automatically considers sources as 0 for t<0, which introduces a transient component. In the case of AC analysis, the LTSpice calculations are effectively $V_0 \sin(\omega t)u(t)$. To avoid including the transient effects in our calculations, we want to start our plots after the transients have attenuated to zero. Typically, several periods will be sufficient. In the above case, a start time of

1ms corresponds to 5 periods after t=0, which is sufficient to ignore the effects of the initial transients. In this example the run time is 1E-4 to 1.6E-4s, with a maximum step size of 1E-6s, allowing 3 full cycles to be displayed. The start time, run time, and step size values depend on the frequency and circuit. <u>Amplitude</u>:

Measured Amplitude (Instrumentation Board and LTSpice): In the case of sinusoidal sources, the magnitude of the voltage is equivalent to the amplitude of the sine wave. Remember, amplitude is half the peak to peak voltage. You can estimate it from the plots or use the measurement tab. It is clear from the plot on the previous page that the amplitude is 5. As we have experienced, the Discovery Board amplitude for the same source voltage setting will probably be a little different.

Calculated Amplitude: The calculated amplitude is determined by applying impedance analysis to the circuit, similar to what was seen in Unit 2. Again, considering the RC circuit and using a voltage divider, we can derive the voltage across the capacitor as

$$V_{C} \Longrightarrow \frac{Z_{C}}{Z_{C} + Z_{R}} (V_{o} \angle \phi) = \frac{\frac{1}{j\omega C}}{\frac{1}{j\omega C}} (V_{o} \angle \phi)$$
$$|V_{C}| = \left| \frac{\frac{1}{j\omega C}}{\frac{1}{j\omega C}} (V_{o} \angle \phi) \right| \rightarrow \left| \frac{1}{1 + j\omega RC} (V_{o} \angle \phi) \right| = \left| \frac{1}{j(2\pi 5E3)(1E3)(1E - 7) + 1} 5 \right| = 1.5$$

<u>Phase</u>: The phase of the waveform is defined as the angle relative to some reference. Typically, the source waveform has zero phase and is used as the reference.

Instrumentation Board (and LTSpice): Recognizing that one period corresponds to 2π (360°), the phase shift can be determined by the time difference between the two peaks. Since the source frequency is 5kHz, one period occurs every 200µs. In seconds, Δt , the phase shift of the capacitor voltage can be measured as the time between when the source peak occurs and the capacitor voltage peak occurs. Using the cursors and recording the Δt , we see a delay (negative Δt) of 40µs for the capacitor voltage. The phase shift is

then $\left(\frac{phase shift}{period}\right)(360) = \left(\frac{-40}{200}\right)(360) = -72^{\circ}$. Note, the capacitor voltage peak occurs <u>after</u> the source peak, indicating a negative phase shift in the capacitor voltage relative to the source. Again, in both Discovery Board and LTSpice, you should use cursors to make these measurements.

Calculated Phase: The calculated phase of the waveform is determined using the phasor form of the complex numbers

Laboratory: RC Circuits:



Implement the above circuit. Set the source amplitude to 2Vpp sinusoid (1V amplitude) with a 0V DC offset.

- a. For a frequency of 2750 Hz, calculate the phasor form of the voltage across the capacitor.
- b. Implement the circuit and obtain the phase and magnitude from the Discovery Board.
- c. Implement the circuit in LTSpice and obtain phase and magnitude from the simulation. Use the Vsin component for the source.
- d. Compare your above results and determine a time domain expression for voltage across the capacitor.
- e. Repeat a. and b. for a frequency of 27850 Hz. At this frequency would you characterize the capacitor as a short circuit? an open circuit? neither?
- f. Repeat a. and b. for a frequency of 50 Hz. At this frequency would you characterize the capacitor as a short circuit? an open circuit? neither?

Include screen shots of your results in your Proof of Concept Report. There are multiple steps here. Arrange and label with headings in an easy to understand way for grading TAs!

B.2: RL Circuits:



Implement the above circuit. Set the source amplitude to 0.2Vpp sinusoid with a 0V DC offset.

- a. For a frequency of 2750 Hz, calculate the phasor form of the voltage across the inductor.
- b. Implement the circuit and obtain the phase and magnitude from the Discovery Board.
- c. Implement the circuit in LTSpice and obtain phase and magnitude from the simulation.
- d. Compare your above results and determine a time domain expression for voltage across the inductor.
- e. Repeat a. and b. for a frequency of 27500 Hz. At this frequency would you characterize the inductor as a short circuit? an open circuit? neither?
- f. Repeat a. and b. for a frequency of 50 Hz. At this frequency would you characterize the inductor as a short circuit? an open circuit? neither?

Include screen shots of your results in your Proof of Concept Report. There are multiple steps here. Arrange and label with headings in an easy to understand way for grading TAs!

PART A: Proof of Concepts list

A.1: RC Circuits – Prove how the capacitor impedance changes with frequency using phasor analysis (phasor form)

A.2: RL Circuits – Prove how the inductor impedance changes with frequency using phasor analysis (phasor form)

Part B: Complex Power

Complex power, S [VA], is defined as the sum of the real power, P [W], and the reactive power, Q [VAR].

$$S = P + jQ$$

Only resistors consume real power and P must be greater than zero. Inductors store reactive power, with Q being positive for inductors and negative for capacitors.



When considering an arbitrary impedance with current Vo and Io, we can use one of the three representations to calculate complex power in phasor form as:

1)
$$S = V_{RMS} I_{RMS}^* = |V_{RMS}| |I_{RMS}| (\angle V - \angle I)$$

2)
$$S = |I_{RMS}|^2 Z = |I_{RMS}|^2 (\angle Z)$$
 (should be multiplied by absolute value of Z)

3)
$$S = \frac{|V_{RMS}|^2}{Z^*} = \frac{|V_{RMS}|^2}{|Z|} (\angle Z)$$

Any of the above equations can be used to find the complex power. Recall, Euler's law can be used to put the expression in rectangular form to find the real and reactive components.





Implement the above circuit. (The above diagram was made in PSpice but you should be able to simulate it in LTSpice). Set the source amplitude to 5V amplitude with a frequency of 795 Hz.

- a. Use phasor analysis to calculate the current through Rs
- b. Using the first power expression, determine the complex power produced by the source. Recall, the current through Rs is equal to the current through Vs since those components are in series.
- c. Using any of the power expressions, determine the complex power in Rs, Z1 and Z2.
- d. Using your instrumentation board, measure the voltage across Rs and use Ohm's Law to determine the current through Rs. Based on this measurement, calculate the power produced.
- e. Measure the voltage across Rs, Z1 and Z2 and use the third expression to determine the power for each.

1. Complete the following table and compare your results.						
	Calculated power	Measure				

f.	Complete t	he following	table and	compare	your results.
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	Calculated power		Measured power	
	Р	jQ	Р	jQ
Rs				
Z1				
Z2				
Vs				

Include these charts in your Proof of Concept report and relevant screen shots for source and at least one load.



Implement the above circuit. (*The above diagram was made in PSpice but you should be able to simulate it in LTSpice*). Set the source amplitude to 5V amplitude with a frequency of 795 Hz. Your capacitor value does not have to be exact. Try to get something reasonably close.

- a. Repeat the previous calculations and measurements for the above circuit.
- b. What do you notice when compare these two circuits? Be sure to answer this in your discussion

	Calculated power		Measured power	
	Р	jQ	Р	jQ
Rs				
Z1				
Z2				
Z3				
Vs				

Include these charts in your Proof of Concept report and relevant screen shots for source and at least one load.

PART B: Proof of Concepts list

B.1: Prove that the power produced by the source balances the power consumed by the load(s).

B.2: Prove that the power produced by the source balances the power consumed by the load(s).

Part C: Filters and Bode Plots

C.1. RL Filter Circuit Calculations



.ac dec 100 1Hz 1E6Hz

Determine the transfer function for the voltage across the resistor,

H_{VR}(s) = _____

Determine the transfer function for the voltage across the inductor,

H_{VL}(s) = _____

What is the cutoff frequency for this circuit?

As the frequency approaches zero (DC), the voltage across the inductor approaches

As the frequency approaches infinity, the voltage across the inductor approaches

When measuring the voltage across the inductor, is this circuit a low pass filter or a high pass filter?

C.2. RL Filter Circuit Simulation and Experiment

Build the circuit. Set your source amplitude to 2Vpp (1V amplitude) with a 0V offset and adjust the frequency as indicated in the table. Measure the output voltage amplitude and phase across the <u>inductor</u> for various frequencies and fill in the following table with your calculations, LTSpice, Instrumentation Board measurements.

		Inductor					
		Magni	itude		Phase [D	Phase [Degrees]	
Freq. [Hz]	Rad. Freq. [rad/s]	Calculated	LTSpice	Measured	Calculated	LTSpice	Measured
47.7							
159							
477							
fc							
1590							
4770							
15.9k							

Include this table of results in your Proof of Concept Report. *There are multiple steps here. Arrange and label with headings in an easy to understand way for grading TAs*!

If there are any significant differences between measured and calculated results, comment on possible reasons. (Recall the characteristics of a real inductor.)

For your above measured results, determine the magnitude in dB and the phase of the transfer function. When calculating the transfer function magnitude, remember to divide to divide the output magnitude by the source amplitude (which in this case is 1). The phase can be obtained directly from your measured results in the previous table.

Radial Frequency [rad/s]	log(ω)	20log H(s) [dB]	Phase H(s)
300			
1E3			
3E3			
ω _c			
1E4			
3E4			
1E5			

Include this table of results in your Proof of Concept Report. *There are multiple steps here. Arrange and label with headings in an easy to understand way for grading TAs*!

Using your measured results, generate a log-log plot (dB-log (ω)) of the magnitude of the transfer function vs. frequency. Also, plot the angle (phase) of the transfer function against log(ω). For the same circuit, perform an AC Sweep in LTSpice and compare your results. Can you find a way to get this information using the Analog Discovery Board or M1K or M2K board directly? How?

Magnitude



Is the cutoff frequency a -3dB point?

Does your stopband display a 20dB/decade drop?

Include screen shots of your results in your Proof of Concept Report. There are multiple steps here. Arrange and label with headings in an easy to understand way for grading TAs!



1 1 1 1 1 1 1 1		
	 <u>↓ </u>	
1	 	
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What is the change of phase between $\omega = 300 \text{ [rad/s]}$ and $\omega = 5\text{E4} \text{ [rad/s]}$? Is your result consistent with expectations from the transfer function?

Include screen shots of your results in your Proof of Concept Report. There are multiple steps here. Arrange and label with headings in an easy to understand way for grading TAs!

PART C: Proof of Concepts list C.1: Prove that gain and phase change with frequency for an RL circuit. C.2: Prove that the Bode Plot for gain and phase reflects this change of frequency for an RL circuit.

Part D: Second Order Filters and Bode Plots

Overview Notes:

Two stage circuits:



The above figure represents a two stage circuit. Recall, the transfer function relates the ratio the input to the output of a stage, $\frac{V_{out}(s)}{V_{in}(s)} = H(s)$. In the above figure, recognize that $V_{out1} = V_{in2}$. The transfer function can be applied to each stage. Applying the transfer function to each stage we can derive the equation, $V_{out2}(s) = H_2(s)V_{in2}(s) = H_2(s)V_{out1}(s) = H_2(s)H_1(s)V_{in1}(s)$. Finally, the relationship between V_{out2} and V_{in1} can be written as $\frac{V_{out2}(s)}{V_{in1}(s)} = H_2(s)H_1(s)$. This equation is the product of the two transfer functions. By designing each stage to produce a particular circuit response, the final output can be designed to meet a specific goal.

Filter types:











D1: Multi-stage Filter Circuit Calculations

Determine the transfer function for the voltage across the resistor, R4.

H(s) = _____(symbolically)

This circuit is which of the following? highpass filter, lowpass filter, bandpass filter, notch filter What are the zeros

of the transfer function?

What are the poles of the transfer function?

What is the gain of the passband, in dB?_____

What is the slope of the stopband(s) in dB/decade?

Multi-stage Filter Circuit Simulation and Experiment

Build the circuit and measure the output voltage for various frequencies. Your source amplitude should 200mV. Your Oscilloscope V/div should be 0.5V/div or less. Fill in the following table with your calculations and Analog Discovery Board measurements. Remember to scale your measured output voltage with your input voltage to obtain the transfer function.

	Magnitude, H(s)				
Frequency [Hz]	Calculated	Measured	20log H(s) (use measured value)		
15.9					
48					
159					
488					
1590					

Using your measured results, generate a Bode plot of the magnitude. Compare the measured results to your analytic expression and LTSpice simulation.

Include screen shots of your results in your Proof of Concept Report. *There are multiple steps here. Arrange and label with headings in an easy to understand way for grading TAs*!

PART D: Proof of Concepts list D.1: Prove the expected Magnitude Bode plot for the multistage circuit.

Part E: Filter Design Problem

Design a lowpass filter that meets the following specifications.

- 1. Cutoff frequency: 1.59kHz
- 2. 60dB rolloff in the stopband
- 3. A single unity gain opamp
- 4. $|H(j\omega)| \rightarrow -3dB$ relative to the passband at the cutoff frequency. (Remember a triple pole has a -9dB correction relative to the straight line approximation. You will need to underdamp a second order circuit.)
- 5. Use L and C component values found in your kit. You can use resistors found on the center table in the laboratory room.

Some flexibility exists in meeting the specifications. In design problems, perfection is not usually possible, but deviations should be small. If you don't quite meet specification, explain why and explain what you would do to fix the problem.

E.1 Filter Design Calculations

Determine the transfer function that meets the above specifications

H(s) = _____(symbolically)

Draw the circuit, labeling the component values

E.1. Filter Design Simulation and Calculations

Build the circuit and verify that the specifications are met by taking measurements at appropriate frequencies. You should base your Vin amplitude around that setting.

	Magnitude, H(s)					
Radial Frequency [rad/s]	Calculated	Measured Vout / Vin	20log H(s) (use measured value)			
100						
300						
1E3						
3E3						
9E3						
10E3						
15E3						
20E3						
30E3						
100E3						

Due to the steep rolloff and noise effects, you may have difficulty obtaining data as you move further into the stopband.

For the same circuit, perform an AC Sweep in LTSpice and compare your results.

Include screen shots of your results in your Proof of Concept Report. *There are multiple steps here. Arrange and label with headings in an easy to understand way for grading TAs*!

PART E: Proof of Concepts list

Prove that your designed filter meets specifications.

Alpha Lab Applications (Extra Credit....) Passive filter vs.

Active filter

- 1. What is the difference between a passive filter and an active filter?
- 2. Find three different active filter configurations. How are they different?
- 3. Describe *any* conditions for which an active filter is much more useful than a passive filter.
- 4. Simulate any active filter configuration. Calculate and compare.

Include screen shots of your results in your Proof of Concept Report. You do not need to build the circuit. It is optional.

SUMMARY of Concepts

Concept List that must be accounted for in your Proof of Concepts

PART A:

- **1.** RC Circuits Prove how the capacitor impedance changes with frequency using phasor analysis (phasor form)
- 2. RL Circuits Prove how the inductor impedance changes with frequency using phasor analysis (phasor form)

PART B:

- 1. Prove that the power produced by the source balances the power consumed by the load(s).
- 2. Prove that the power produced by the source balances the power consumed by the load(s).

PART C:

- 1. Prove that gain and phase change with frequency for an RL circuit.
- 2. Prove that the Bode Plot for gain and phase reflects this change of frequency for an RL circuit.

PART D:

1. Prove the expected Magnitude Bode plot for the multistage circuit.

PART E:

1. Prove that your designed filter meets specifications.